



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

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PATENTS
ALT-155
TC 2700 MAIL ROOM

Applicants : Tony K. Ngai et al.
Application No.: 09/124,649
Filed : July 29, 1998
For : PROGRAMMABLE LOGIC DEVICE HAVING
EMBEDDED DUAL-PORT RANDOM ACCESS MEMORY
CONFIGURABLE AS SINGLE-PORT MEMORY
Group Art Unit : 2751

Hon. Assistant Commissioner
for Patents
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b),
applicants hereby make the following publications of record
in the above-identified patent application:

Nance et al. U.S. Patent 5,715,197 (February 3,
1998)
Young U.S. Patent 5,933,023 (August 3, 1999)
Sample et al. U.S. Patent 6,011,744 (January 4,
2000)

1999 Xilinx Databook, 1999, Xilinx, Inc., San
Jose, California

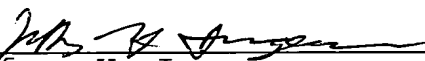
Copies of the aforementioned patents and publications, which
are listed on the accompanying Form PTO-1449 (submitted in
duplicate), are enclosed herewith.

It is respectfully requested that these patents
and publications be (1) fully considered by the Patent and
Trademark Office during the examination of this application;
and (2) printed on any patent which may issue on this

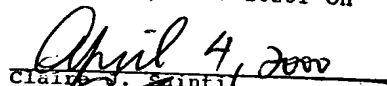

application. Applicants request that a copy of Form PTO-1449, as considered and initialled by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

Respectfully submitted,


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